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DB=USPT; PLUR=YES; OP=ADJ

<u>L11</u>	6871268.pn.	1	<u>L11</u>
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<u>L10</u>	L9 not @AD>20031117	11	<u>L10</u>
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<u>L9</u>	L8 not l6	14	<u>L9</u>
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<u>L8</u>	L7 and (time near5 \$validat\$)	14	<u>L8</u>
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<u>L7</u>	(consisten\$3 near5 object\$) and (delay\$ near5 (updat\$ or modif\$))	71	<u>L7</u>
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<u>L6</u>	L5 and (time near5 \$validat\$4)	14	<u>L6</u>
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<u>L5</u>	L4 not @AD>20031117	51	<u>L5</u>
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<u>L4</u>	(time and \$valida\$4 and consist\$ and (updat\$ or modif\$)).ab.	57	<u>L4</u>
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<u>L3</u>	L2 and (time near5 \$validat\$)	0	<u>L3</u>
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<u>L2</u>	L1 and (updat\$ near5 version\$)	0	<u>L2</u>
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<u>L1</u>	(5528511 or 20020194382).pn.	2	<u>L1</u>
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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Persistent caching: an implementation technique for complex objects with**
Kato, K.; Masuda, T.;
[Software Engineering, IEEE Transactions on](#)
Volume 18, Issue 7, July 1992 Page(s):631 - 645
Digital Object Identifier 10.1109/32.148481
[AbstractPlus](#) | [Full Text: PDF\(1376 KB\)](#) IEEE JNL
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- ☐ 2. **Proxy cache algorithms: design, implementation, and performance**
Shim, J.; Scheuermann, P.; Vingralek, R.;
[Knowledge and Data Engineering, IEEE Transactions on](#)
Volume 11, Issue 4, July-Aug. 1999 Page(s):549 - 562
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[AbstractPlus](#) | [References](#) | [Full Text: PDF\(1132 KB\)](#) IEEE JNL
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- ☐ 3. **Efficient optimistic concurrency control for mobile real-time transactions data broadcast environment**
Li Guohui; Yang Bing; Chen Jixiong;
[Embedded and Real-Time Computing Systems and Applications, 2005. Proceedings. International Conference on](#)
17-19 Aug. 2005 Page(s):443 - 446
Digital Object Identifier 10.1109/RTCSA.2005.42
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- ☐ 4. **Programming Language Inherent Support for Constrained XML Schema Types and OWL DL**
Paar, A.; Tichy, W.F.;
[Automated Software Engineering, 2006. ASE '06. 21st IEEE/ACM International](#)
Sept. 2006 Page(s):281 - 284
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- ☐ 5. **Distributed Java Virtual Machine for message passing architectures**
Surdeanu, M.;
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6. A data-centric concurrency control mechanism for three tier systems

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